

Embedded System Hardware Design Training - Advanced	
Day	Topic
Day1 (Morning)	<ul style="list-style-type: none"> <li>• Understanding the SRS</li> <li>• Identification of class A components</li> </ul>
Day1 (Afternoon)	<ul style="list-style-type: none"> <li>• Architecting</li> <li>• Power estimation</li> </ul>
Day2 (Morning)	<ul style="list-style-type: none"> <li>• I/O estimation</li> <li>• Creating HDD from SRS</li> </ul>
Day2 (Afternoon)	<ul style="list-style-type: none"> <li>• Power supply conversion               <ul style="list-style-type: none"> <li>◦ Advanced topics in AC to DC</li> <li>◦ Advanced topics in DC to DC</li> </ul> </li> </ul>
Day3 (Morning)	<ul style="list-style-type: none"> <li>• Digital I/O               <ul style="list-style-type: none"> <li>◦ High speed Interfaces</li> </ul> </li> </ul>
Day3 (Afternoon)	<ul style="list-style-type: none"> <li>• Analog I/O               <ul style="list-style-type: none"> <li>◦ Voltage I/O and Current loop</li> <li>◦ Signal conditioning</li> </ul> </li> </ul>
Day4 (Morning)	<ul style="list-style-type: none"> <li>• Memory Interface               <ul style="list-style-type: none"> <li>◦ DDR2/3</li> </ul> </li> </ul>
Day4 (Afternoon)	<ul style="list-style-type: none"> <li>• Serial communication Interface               <ul style="list-style-type: none"> <li>◦ CAN</li> <li>◦ USB</li> <li>◦ LAN</li> </ul> </li> </ul>
Day5 (Morning)	<ul style="list-style-type: none"> <li>• Signal Integrity analysis</li> </ul>
Day5 (Afternoon)	<ul style="list-style-type: none"> <li>• Signal Integrity analysis</li> </ul>
Day6 (Morning)	<ul style="list-style-type: none"> <li>• Thermal Analysis</li> </ul>
Day6 (Afternoon)	<ul style="list-style-type: none"> <li>• Thermal Analysis</li> </ul>
Day7 (Morning)	<ul style="list-style-type: none"> <li>• EMI/EMC Compliance</li> </ul>
Day7 (Afternoon)	<ul style="list-style-type: none"> <li>• EMI/EMC Compliance</li> </ul>